LF noise in cross Hall effect devices geometrical study

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The aim - to optimize the shape of the cross-shaped Hall devices with regard to conductivity noise generated on sensing contacts

The result - conductivity noise reduction due to pure geometrical effect applicable to Hall sensors based on various physical structures like:

- AlGaAs/InGaAs/GaAs magnetic sensors attend to [5115-26] !
- FD-SOI magnetic sensors attend to [5115-27] !

Related to [5115-26] & [5115-27] (oral session 6, Ballroom North, Tuesday 3:35 to 4:55 pm) [5115-26] - 3:35 pm: "Intrinsic low-frequency noise in AlGaAs/InGaAs/GaAs micro Hall sensors" [5115-27] - 3:55 pm: "Low-noise SOI Hall devices"

Work done:

Idea - to smooth horizontal current flow and cut corners of the standard cross-shaped Hall device:



Figure 1. Shape of the Hall effect device. w, l, c are parameters which determines the shape of our sample

Calculations:

- adjoint network approach
- calculations of current and adjoint current distributions
- geometrical factors





 Contour plot for dep geometrical correction f Hall voltage on parameters c/l and w/l.

conduction noise generated on sensing and on driving current contacts (S_0 and S_D , respectively) is a function of the current density and adjoint current density distributions in the sample



ntour plot for pois sie power density for sensing contacts for samples with unitide length ent. Units are decibels (HB) plus arbitrary constant i.e. only differe paring these results with those depicted in Fig. 2 and 3 enable us to re-nore than 5 dB as compared to a Hall cross with sharp 90° angles. o of the 2-dimensional sample while keeping the geometrical corre-shown as distinguished line with geometrical correction factor for

Experimental

Samples

we let the sensor shape continuously evolve from a plain Greek cross (1) with sharp angle to a diamond-shape with small co tacts (7)

Sample name	Length I (gen)	Width (p m)	Cat Corner (pn)	Reduced width wi	Relaced cut corner cl	Sample number	Shape
LWC42	320	84	0	0.263	0.000	1	
LWC40	320	80	20	0.250	0.063	2	ΕI
LWC37	320	74	40	0.231	0.125	3	H
LWC72G	720	144	180	0.200	0.250	4	H
LWC30	320	60	80	0.188	0.250	5	H
LWC22	320	44	112	0.138	0.350	6	\bullet
LWC17	320	34	128	0.106	0.400	7	

Table 1. Geometrical parameters of the samples. Denotat ed in Figure 1. / is the length of the sample

Measurements



ollected or Bi and the

Summary of the Results





nuously evolve from a plain Gro ve let the sensor shape co acts. The output voltage noise power spectrum density first starts to decrease sharply und a set of dimensions corresponding to the shape denoted as LWC30 in this study

Conclusions

We have performed a systematic study of the poise level for series of devices with constant equivalent performed a systemate study of the hole rever to series of the toes with constant equivalent asp ance and geometrical correction factor for Hall voltage. The influence of the device geometry on t vell predicted by the approach of adjoint network theory. The optimization of the shape of four ce with regard to noise power density observed on its sensing contacts can reduce the noise power by more than 5 dB while keeping the same overall lateral device dime an optimized shape, the surface occupied by the sensor (and thus the by a factor of almost 4 as compared to a standard Greek cross, while ions. On the other side this m same overall rateral device unitensions. On the other side this mean sied by the sensor (and thus the semiconducting material costs) car to a standard Greek cross, while keeping the same level of electro

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